



Learn Beyond

KPR Institute of Engineering and Technology

(Autonomous, NAAC "A")

Avinashi Road, Arasur, Coimbatore.

Phone: 0422-2635600**Web:** kpriet.ac.in**Social:** kpriet.ac.in/social**EC001****NBA Accredited**
(CSE, ECE, EEE,
MECH, CIVIL)**EXPERT TALK " ASIC DESIGN FLOW AND METHODOLOGY IN VLSI ENGINEERING "**

Event No	EC001
Organizing Department	Electronics and Communication Engineering
Date	08/02/2024
Time	10:00 AM to 11:00 AM
Event Type	Expert Talk
Event Level	Dept. Level
Venue	D-105
Total Participants	60
Students - Internal	60

Related SDG**Resource Persons**

Sl	Type	Name	Designation	Company	Email	Phone
1	Chief Guest	Mr B K Shivaprasad	Sr.Field Application Engineer	Entuple Technologies	shivaprasad.bk@entuple.com	xxxxxxxxxx

Involved Staffs

Sl	Name	Role
1	Kathirvelu M	Convenor
2	Muralidharan J	Co-convenor
3	Prasad J	Co-convenor

Outcome

At the end of the session, students were able to understand ASIC design flow
The Role of ASIC in VLSI Engineering
Applications of VLSI in the healthcare and automobile industries

Event Summary

The department of Electronics and Communication Engineering students' association SPARTRANZ organizes an expert talk on "ASIC design flow and methodologies in VLSI engineering" on February 8, 2024, in D-105. Mr. Shivprasad, Sr. Field Engineer from Entuple Technologies Bangalore, was the industry expert. This expert talk is organized for third-year ECE students to enhance their knowledge in the VLSI domain, in which they are studying the VLSI design course in the current semester. The session started with a welcome, the gathering, and an introduction about the industry expert, then the session handover to the expert. The expert starts his presentation with an overview of Entuple Technologies and their service to society. The following topics are covered during his presentation: VLSI: An Overview
List of semiconductor materials used in integrated circuit fabrication
Semiconductor material growth in the market
What is ASIC and its roles in VLSI Engineering?
List of companies that offer VLSI jobs
After his presentation was over, the students actively interacted with the expert through a question-and-answer session. The faculty member in charge of the event, Dr.J. Prasad, AP (Sl.G.)/ECE, read the vote of thanks.



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INDUSTRY EXPERT TALK
on
ASIC design flow and Methodology in VLSI Engineering

Mr.B.K.Shivaprasad
Sr.Field Application Engineer,
Entuple Technologies,
Bengaluru.

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



08.02.2024

III ECE - B (D - 105)

10.00AM to 11.00AM



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